

Ω-FRG / Forge

Self-fabrication/semiconductor-engineering SoC

IP-4937 · Integrated reference · SoC architecture + source

Function and architecture

- timing
- placement
- routing search
- DRC/PDN proxies
- formal/search acceleration
- native engineering

Integration summary

Integration scope, interfaces and qualification are agreed for the selected release.

Technical record

Canonical identity	IP-4937
Responsibility	Self-fabrication/semiconductor-engineering SoC
Delivery boundary	SoC architecture + source
Evidence scope	Integrated reference

Delivery and release binding

- Constituent release manifest
- Topology and physical integration assumptions
- System software and orchestration boundaries
- Qualification scope and system-level acceptance

Confirm for the selected release: Environment and dependencies, Version / build hash, API compatibility, License and support scope.

Evidence and qualification

The V29 estate records a structurally closed 17-SoC hierarchy and native software/reference-model evidence.

V29 inherited structural RTL and model evidence do not constitute a fresh foundry physical-signoff or fabricated-product result.

Evaluation and licensing

Evaluation: System architecture evaluation. Production: Platform integration program. Custom work: Customer-specific system composition. Rights, pricing, support and acceptance criteria are agreed for the exact scope.

Contact: patrick@garmolabs.com | Product path: /system/frg-forge